

Applicant: Taylor R. Efland

Filed: 10/12/01

Serial No.: 09/975,630

Examiner: Andujar, L. Circuit Structure Integrating the Power Distribution Functions of Circuits and

Leadframes into the Chip Surface

SUPPLEMENT TO AMENDMENT FILED UNDER 35 U.S.C. 111

February 24, 2003

Assistant Commissioner For Patents Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

Docket:

Art Unit:

I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: oner for Patents, Washington, DC 20231

Michael K. Skrehot, Reg. No. 36,682

TI-31678

2826

Applicant inadvertently omitted the proposed drawings from the enveloped mailed even date herewith containing a response to the Office Action mailed 10/24/02. drawings are attached. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

Michael K. Skrehot

Registration No. 36,682

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MAR - 5 2003